10th EDITION
DESIGN AND VERIFICATION DVCON CONFERENCE AND EXHIBITION INDIA

accellera SYSTEMS INITIATIVE

Certification of Appreciation

Presented to

Swapnil Sindhur

Vayavya Labs

With appreciation for your **Poster** contribution titled

Bridging RISC-V Core Verification and PSS: A

Portable-Stimulus Stress-Testing Approach

Architecture to Analytics A2A

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