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DESIGN AND VERIFICATION DVCON CONFERENCE AND EXHIBITION INDIA

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Certification of Appreciation

Presented to

Gowri Shankar Somanjeri

Intel

With appreciation for your **Poster** contribution titled

Securing and Optimizing Data Flow by Validating Die Level Interconnect Stress with AES XTS 256-bit Encryption and Ring Interconnect Analysis on edge AI Architecture to Analytics AI SoCs

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