10th EDITION
DESIGN AND VERIFICATION DVCON CONFERENCE AND EXHIBITION INDIA

accellera SYSTEMS INITIATIVE

Certification of Appreciation

Presented to

Vikas Vikram Singh

Qualcomm

With appreciation for your **Poster** contribution titled

Novel Use of Symbolic Map Based Input Driving for Synchronization Block Verification Using Formal Methods

Pradeep Salla
General Chair - DVCon India 2024

Architecture to Analytics A2A
DVCON 2025
September 10-11, 2025 I Radisson Blu, Marathahalli, Bangalore

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